Panasonic

PNA4U23F (Tentative)

Photodiode with amplifier functions

For optical control systems

Features

- Small package, × 52 speed
- Reflow soldering possible

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit
Operating supply voltage	V _{CC}	6	V
Power dissipation	PD	250	mW
Operating ambient temperature	T _{opr}	-20 to +70	°C
Storage temperature	T _{stg}	-40 to +85	°C

Operatong Condition

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Operating supply voltage	V _{CC}		4.5	5.0	5.5	V
Reference voltage	V _{REF}		1.55	1.65	1.75	V

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$, $V_{CC} = 5.0$ V, $R_L = 10$ k Ω , $C_L = 20$ pF, $V_R = 300 \Omega$

Parameter	Symbol	Conditions		Min	Тур	Max	Unit
CWV share south as more so	V_{SW1}	CD	$Gain1 / Gain2 \rightarrow Gain1$	2.4		V _{CC}	V
SW change voltage range	V_{SW2}	DVD	$Gain1 / Gain2 \rightarrow Gain2$	0		0.99	V
Output offset voltage	V _{OFF}	[V _O P-	V _O N] No signal condition	-20	0	20	mV
Maximum output voltage *2	V _{OM}	[V _O P-	V ₀ N] Max. Reference to GND	2.0	2.2		V
Maximum output voltage (+) *3	V _{OM+}	[V _O P-	V ₀ N] Max. Reference to GND	1.0	1.1		V
Maximum output voltage $(-)^{*3}$	V _{OM-}	[V _O P-	$[V_0P - V_0N]$ Max. Reference to GND		-1.1		V
Output sensitivity *1	Gain1	$[V_{O}P - V_{O}N] \lambda = 780 \text{ nm}$		2.1	2.8	3.5	V/mW
Output sensitivity	Gain2	$[V_OP - V_ON] \lambda = 650 \text{ nm}$		3.3	4.4	5.5	V/mW
Supply current	I _{CC}	No signal condition		—	26.0	29.9	mA
Cutoff frequency $f_{C(-3dB)}$	CD	Gain1 20 log (V_O (f_C MHz) / V_O (1 MHz)) = -3	80	90		MHz	
	DVD	Gain2 20 log (V_O (f_C MHz) / V_O (1 MHz)) = -3	70	80		MHz	
Rise time t _r	4	CD	$V_{O}P - V_{O}N = 2 V[p-p], 10\% \text{ to } 90\%, \text{Gain1}$		5		ns
	ι _r	DVD	$V_{O}P - V_{O}N = 2 V[p-p], 10\% \text{ to } 90\%, \text{Gain}2$		5	_	ns
Fall time		CD	$V_{O}P - V_{O}N = 2 V[p-p], 10\% \text{ to } 90\%, \text{ Gain1}$		5		ns
	t _f	DVD	$V_0P - V_0N = 2 V[p-p], 10\%$ to 90%, Gain2	_	5	_	ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

2. *1: Standard voltage level; V_{REF} (Exclude output offset voltage)

*2: Full saturation value

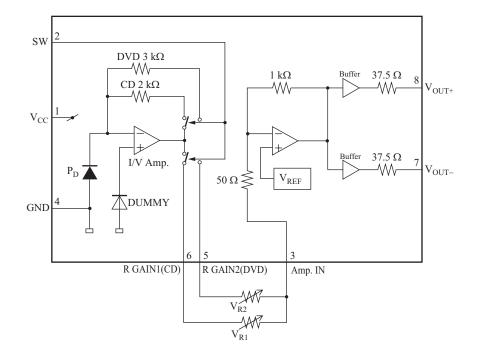
*3: Linearity < 1%

Electrical Characteristics (Continued) $T_a = 25$	$^{\circ}C\pm 3^{\circ}C$, $V_{CC} = 5.0 \text{ V}$, $R_{L} = 10 \text{ k}\Omega$, $C_{L} = 20 \text{ pF}$, $V_{R} = 300 \Omega$
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Parameter	Symbol	Conditions		Min	Тур	Max	Unit
Slew rate	SR			200	300	_	V/µs
O dilling disc		CD	$V_0P - V_0N = 2 V[p-p]$ at Gain1, ±3%		12		ns
Settling time t _{set} DV	DVD	$V_0P - V_0N = 2 V[p-p]$ at Gain2, ±3%		14		ns	
Mode selecting time	t _{sel}	$Gain-high \leftrightarrow Sleep \leftrightarrow Low$			150	200	ns

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

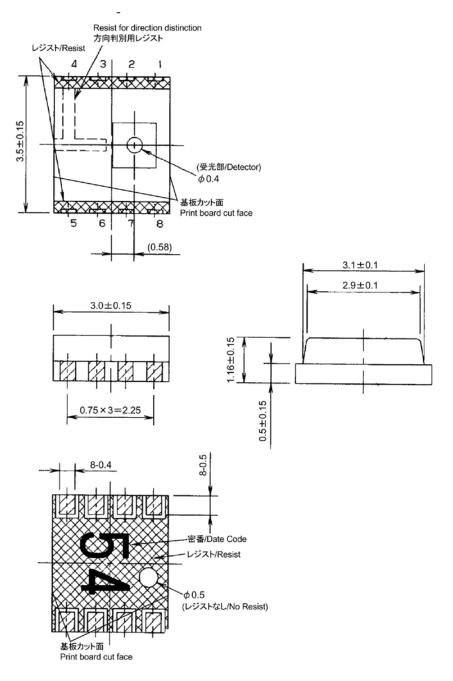
Block Diagram



Panasonic

Package (Unit: mm)

KPTFTN6K0004



• Pin name

1: V _{CC}	5: R GAIN2
2: SW	6: R GAIN1
3: Amp.IN	7: V _{OUT-}
4: GND	8: V _{OUT+}

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